

Please amend the subject application as follows:

IN THE ABSTRACT

Please amend the Abstract of the Disclosure as follows:

--A semiconductor memory device including a plurality of normal cell blocks including a plurality of normal memory cells for storing data, a plurality of redundancy cell blocks--including, a plurality of redundancy memory cells for substituting for defective normal memory cells, and at least one ECC cell block including a plurality of ECC memory cells, for substituting for defective normal memory cells in response to a mode signal.--